

Name:	 UPES UNIVERSITY WITH A PURPOSE
Enrolment No:	

UNIVERSITY OF PETROLEUM AND ENERGY STUDIES
End Semester Examination, May 2019

Course: Resource allocation on mainframe
Program: B.Tech CSE MT
Course Code: CSIB 462

Semester: VIII
Time 03 hrs.
Max. Marks: 100

Instructions: All questions are compulsory.

SECTION A

S. No.		Marks	CO
Q 1	Describe the various functions of Hardware configuration definition.	4	CO1
Q 2	Define the Dynamic logical Partitioning problems (DLPAR).	4	CO4
Q 3	Explain the three phases of first failure data capture collection process in problem analysis.	4	CO2
Q 4	Illustrate the Understanding of parallel access Volume.	4	CO5
Q 5	Describe the Asynchronous Transfer Mode.	4	CO2

SECTION B

Q 6	“Input data sets can migrate using the HCD batch utility instead of the HCD dialog” Justify the statement by explaining the each step in detail.	10	CO1
Q 7	Explain the Fiber optic channels, Architecture and various technologies that support the fiber optic network.	10	CO3
Q 8	Describe the HMC customization of network setting panel, Processor panel of an LPAR image level and customization of security profile.	10	CO4
Q 9	Give the brief description for characteristics of ESCON environment.	10	CO4, CO5
OR			
	Describe the various channel types and concept of logical control units with the help of diagram.		

SECTION-C

Q 10	“An optical fiber is similar to a human hair- a thin strand of glass wire with a hole in it” justify the statement by explaining the optical networking, interconnection and transmitting lights on fiber in different modes.	20	CO5, CO3
Q 11	“System network architecture defines the rules for management and transportation of message through network” justify the statement by explaining the SNA data stream,	20	CO3, CO4, CO5

links, node types and network addressable unit.

OR

“Processors can communicate through shared ESCON or FICON channel paths”
justify the statement in the context of CTC connection and channel path definitions
for ESCON and FICON.

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SECTION A

S. No.	Question	Marks	CO
Q 1	Define the Typical HMC Operational Errors.	4	CO2
Q 2	Describe the HCD in Dialog mode.	4	CO1
Q 3	Illustrate the Network control programs and peripheral nodes.	4	CO3
Q 4	Describe the problem analysis suspend function	4	CO4
Q 5	Describe the main areas under the classic style user Interface window.	4	CO2

SECTION B

Q 6	Compare the Key feature of system Z family of Processors as well as explain the problem analysis framework.	10	CO4
Q 7	Describe the customization of activation profile in HMC, activation completion panel and deactivation completion panel.	10	CO3
Q 8	Demonstrate function of HMC and SE in detail as well as draw the diagram for support element local configuration.	10	CO5
Q 9	Describe the procedure to handle IPL Process when it stalls without any messages along with explain HMC navigation techniques to prevent the errors. OR Compare the Key feature of system Z family of Processors as well as explain the problem analysis framework.	10	CO4, CO5

SECTION-C

Q 10	“When the system hardware is ready, you can use the system console to load the system software” comment on the statement as well as explain the types of IPL.	20	CO5
Q 11	Describe the LPAR Level performance, various performance related issues, typical causes and constraint. OR	20	CO3, CO4, CO5

	<p>“Sometime a given LPAR function may rely on the services of other LPARs being on SYSPLEX” Justify the statement along with that discuss the various constraint, typical causes and performance issues</p>		
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