| Name: <br> Enrolment No: |  |  |  |
| :---: | :---: | :---: | :---: |
| UPES  <br> End Semester Examination, December 2023  <br> Course: Microprocessor and Embedded systems Semester: V <br> Program: B.Tech Computer Science Time : 03 hrs <br> Course Code: ECEG3052 Max. Marks: $\mathbf{1 0 0}$ <br> Instructions:  |  |  |  |
| $\begin{gathered} \text { SECTION A } \\ (5 \mathrm{Q} \times 4 \mathrm{M}=20 \mathrm{Marks}) \\ \hline \end{gathered}$ |  |  |  |
| S. No. |  | Marks | CO |
| Q 1 | Implement the full-adder circuit with a decoder or multiplexer. | 5 | CO1 |
| Q 2 | State the function of given 8085 instructions: JP, JPE, JPO, JNZ, STAX | 5 | CO2 |
| Q 3 | Implement logic functions $\mathrm{F}_{1}(\mathrm{~A}, \mathrm{~B}, \mathrm{C}, \mathrm{D})=\Sigma \mathrm{m}(0,2,7,9,11,13)$ using multiplexer or decoder | 5 | CO1 |
| Q 4 | Explain the operation of JK Flip-Flop with Truth table. | 5 | CO2 |
| Q 5 | Write a program in 8085 microprocessor to access data 32 H and 45 H from 2501 H and 2502 H , add them and store the result in memory location 2503 H . | 5 | CO4 |
| $\begin{gathered} \text { SECTION B } \\ \text { (4Qx10M=40 Marks) } \end{gathered}$ |  |  |  |
| Q 6 | Draw the pin diagram of 8051 and describe the operation of all the pins of 8051 microcontroller. | 10 | CO2 |
| Q 7 | What are the different addressing modes of 8051 microcontroller? Illustrate with example. | 10 | CO 2 |
| Q 8 | What is RISC and CISC architecture? With a neat diagram, explain the difference between RISC and CISC architecture used in embedded systems hardware design. | 10 | $\mathrm{CO5}$ |
| Q 9 | Explain different characteristics of embedded systems. <br> OR <br> Specify the content of registers, flags, output at port1, and calculate the total number of T states required if the following program of 8085 microprocessor is executed. <br> MVI B, 82 H <br> MOV A,B <br> MOV C,A <br> MVI D, 37H <br> OUT PORT1 | 10 | $\underset{5}{\mathrm{CO} 4 / \mathrm{CO}}$ |


|  | HLT |  |  |
| :---: | :---: | :---: | :---: |
| $\begin{gathered} \text { SECTION-C } \\ \text { (2Qx20M=40 Marks) } \end{gathered}$ |  |  |  |
| Q 10 | a) Ten hex numbers are stored in RAM location 50 H onwards. Write a program in 8051 microcontroller to find the biggest number in the set. The biggest number should finally be saved in 60 H . <br> b) In a semester, a student has to take six courses. The marks of the student out of 25 are stored in RAM location 47 H onwards. Find the average marks and output it to port1. | 10 $10$ | $\underset{4}{\mathrm{CO} / \mathrm{CO}}$ |
| Q 11 | a) Design an interfacing scheme for interfacing Analog to digital converter with Microprocessor/ Microcontroller <br> b) Draw the timing diagram of the following instruction: 2000 MOV A,B <br> OR <br> a) Assume that P1 is an input port connected to a temperature sensor. Write a program to read the temperature and test it for the value 75. According to the test results, place the temperature value into the registers indicated by the following. $\begin{array}{lll} \text { IF } & T=75 & \text { then } A=75 \\ \text { IF } & T<75 & \text { then R1 }=\mathrm{T} \\ \text { IF } & T>75 & \text { then R2 }=\mathrm{T} \end{array}$ <br> b) How will you execute multiple interrupt using priority encoder? <br> Develop a circuit to implement the instruction RST 5 using 8085 interrupt. | $10+10$ $10+10$ | $\begin{gathered} \mathrm{CO} 4 / \mathrm{CO} \\ 5 \end{gathered}$ |

