Name:	Compared manufactor in a second of a second of its second		
Enrolment No:			
	UNIVERSITY OF PETROLEUM AND ENERGY STUDIES		
G	End Semester Examination, December 2020		
Course:Digital ElectronicsSemester: IIIProgram:B. Tech ELETime 03 hrs.			
Course Code: ECEG 2016 Max. Marks: 100			
Course		0	
Instructions: Answer all the questions.			
Diagrams must be neat and clean.			
	SECTION A		
	SECTION A		
	Question will carry 5 Marks		
Instru S. No	ction: Complete the statement / Select the correct answer(s)/write a few words	СО	
	Convert the following estal numbers to bingry equivalent	CO	
Q 1	Convert the following octal numbers to binary equivalent i) 132.456 ii) 345.301	CO1	
	1, 102.100 1,010.001	001	
Q 2	Represent the following decimal numbers in 2's complement representation using 8 bits		
x -	i) -64 ii) 67	CO1	
Q 3	Simplify the following expression :	CO2	
	Y=sigma m(0,1,2,3,4,5,6)		
Q 4	Find the Gray codes for the following binary numbers:	CO1	
	a) 10001000 b) 01011100		
Q5	Find the minterms of the function $Y(A,B,C) = A\overline{B} + \overline{C}$	CO3	
Q6	Explain in brief about the semiconductor memories	CO3	
SECTION B			
Each question will carry 10 marks			
	ction: Write short / brief notes	004	
Q 1	Design a combinational logic circuit with four input variables that will produce logic 1 when the number of 1s in the input is ODD.Implement a full subtractor using two 4:1 MUX	CO4	
Q 2	Minimize the following logic function using K-Map :	CO2	
	i)Y(A,B,C,D) =sigma m (,1,2,3,5,7,8,,13,14)		

	ii)Y(A,B,C,D)=pi M(0,2,8,,12,13,14)	
Q 3	Design a mod 8 ripple counter using T-Flipflop and draw the output waveforms	CO4
Q 4	Implement the following output functions using a suitable PLA $F1(A,B,C,D)=sigma m(3,7,8,9,11,14)$ $F2(A,B,C,D)=sigma m(3,4,5,7,11,14,15)$ $F3(A,B,C,D)=sigma m(1,5,6,11,15)$	CO3
Q 5	Explain the operation of R-2R ladder D/A converter and weighted resistor D/A converter. Also Explain the Flash type A/D converter	CO4
	SECTION-C	
	Question carries 20 Marks. action: Write long answer.	
Q 1	a) Design a two bit comparator using suitable decoder b)Develop and analyze a Parallel in parallel out and parallel in serial out shift register with JK-Flip flop	CO4
