## **QUESTION PAPER**

Name:

**Enrolment No:** 

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## UNIVERSITY OF PETROLEUM AND ENERGY STUDIES End Semester Examination, December 2018

**SECTION A (20 Marks)** 

Course: Analog and Digital Electronics (ECEG-2024) Program: B. Tech- Electrical Engineering Time: 03 hrs.

Semester: III

Max. Marks: 100

**Instructions:** Attempt all the sections.

S. No.	Answer all the questions.	Marks	CO
Q 1	Define a hole in semiconductor and indicate pictorially how it contributes in conduction?	4	CO1
Q 2	Elucidate the two basic processes which are responsible for the movement of electrons and holes in a semiconductor.	4	CO1
Q 3	Determine if the diode (ideal) in Fig. (1) is forward bias or reverse biased.	4	CO2
Q 4	Minimize and implement the following multiple output functions using K-map $F_1 = \sum m (1, 2, 3, 6, 8, 12, 14, 15)$ $F_2 = \prod M (0, 4, 9, 10, 11, 14, 15)$	4	CO3
Q 5	Attempt all the parts: (i) $(11011)_2 \rightarrow (?)_{\text{Gray code}}$ (ii) $(10011)_{\text{Gray code}} \rightarrow (?)_2$ (iii) $(126)_{10} \rightarrow (?)_{\text{Excess-3}}$	4	CO3

	$(iv) (1367)_{BCD} \rightarrow (?)_2$		
	SECTION B (40 Marks)		
	Answer all the questions.		
Q 6	A binary ripple counter is required to count up to (16,383) <sub>10</sub> . How many FFs are required? If the clock frequency is 8.192MHz, What is frequency at the output of the MSB?	10	CO4
Q 7	<ul> <li>Attempt both the parts:</li> <li>(a) How does photo diode differ from an ordinary diode? Analyze the performance characteristics of the photo diode.</li> <li>(b) Derive the expression for the efficiency of a full-wave rectifier.</li> </ul>	10	CO2
Q 8	A semiconductor diode having ideal forward and reverse characteristics is used in a half wave rectifier circuit supplying a resistive load of 1000 ohm. If the r.m.s. value is 250V, determine (i) the r.m.s. value of diode current (ii) power dissipated in the load.	10	CO2
Q 9	Determine the operating point of the transistor biasing circuit shown in Fig. (2) by using Thevenin theorem.	10	CO3
	$+ V_{CC} = 15 V$ $10 k\Omega$ $1 k\Omega$ $5 k\Omega$ $2 k\Omega$ Fig. (2)		
	OR		
	Reduce both the Boolean expressions as, $Y = A \left[ B + \overline{C} \left( \overline{AB + A\overline{C}} \right) \right]$ (i)		
	(ii) $Y = (B+BC)(B+\overline{B}C)(B+D)$		

	<b>SECTION-C (40 Marks)</b>		
	Answer all the questions.		
Q 10	You are presented with a set of requirements under which an insurance policy can be issued.		
	<ul> <li>The applicant must be:</li> <li>1. a married female 25 years old or over, or</li> <li>2. a female under 25, or</li> <li>3. a married male under 25 who has not been involved in a car accident, or</li> <li>4. a married male under 25 who has not been involved in a car accident, or</li> <li>5. a married male 25 years or over who has not been involved in a car accident.</li> <li>Find an algebraic expression which assumes a value '1' whenever the policy is issued. Simplify the expression obtained and design logic diagram for the same.</li> </ul>	15+5	CO3
Q 11	Attempt both the parts:		
	<ul> <li>a) Design a synchronous BCD counter using J-K flip flops.</li> <li>b) Design a logic circuit with 4 inputs A, B, C, D that will produce output '1' only whenever two adjacent input variables are 1s. "A and D are also to be treated as adjacent." Implement it using 'OR' universal logic gate.</li> </ul>	10+10	CO4/ CO3
	OR		
	Design a circuit that can be built using AOI logic and output is '1' when a 4-bit BCD code translated to a number that uses the upper right segment of a single 7-Segment display system.		

Name: Enrolment No:       UNIVERSITY OF PETROLEUM AND ENERGY STUDIES End Semester Examination, December 2018         Course: Analog and Digital Electronics (ECEG-2024) Program: B. Tech-Electrical Engineering Time: 03 hrs.       Semester: III Max. Marks: 100         Instructions: Attempt all the sections.         Section A (20 Marks)         Q2         Determine the state of diode for the circuit shown in Fig. (1) and find ID and VD. Assume simplified model for the diode.         4 CO2         Fig. (1)         Q3       Minimize the following multiple output functions F <sub>1</sub> = $\Sigma m$ (0, 2, 6, 10, 11, 12, 13)+ d (3, 4, 5, 14, 15) F <sub>2</sub> = $\Sigma m$ (1, 2, 6, 7, 8, 13, 14, 15)+ d (3, 5, 15)       4       CO3         Q4       (i) (123) <sub>10</sub> $\rightarrow$ (?) convents       4       CO3         Q4       (i) (123) <sub>10</sub> $\rightarrow$ (?) convents       4       CO3         Q4       (i) (123) <sub>10</sub> $\rightarrow$ (?) convents       4       CO3	Name:			
End Semester Examination, December 2018         Semester: III         Program: B. Tech- Electrical Engineering Time: 03 hrs.       Semester: III         Max. Marks: 100         Instructions: Attempt all the sections.         SECTION A (20 Marks)         SECTION A (20 Marks)         SECTION A (20 Marks)         Q and No does a tunnel diode work and analyze tunneling effect in tunnel diode with the performance characteristics.       Q and VD.         Q 2       Determine the state of diode for the circuit shown in Fig. (1) and find ID and VD.         Assume simplified model for the diode.         INOP INCLUSION         Q and Minimize the following multiple output functions         Fig. (1)         Q and Minimize the following multiple output functions         Fig. (1)         Q and Minimize the following multiple output functions         Fig. (1)         Q and Minimize the following multiple output functions         Fig. (1)       4       CO3         Q 4       (i) (123) <sub>10</sub> $\rightarrow$ (?) <i>Locus-3</i> (ii) (11010) <sub>2</sub> $\rightarrow$ (?) <i>Locus-3</i> (ii) (11010) <sub>2</sub> $\rightarrow$ (?) <i>Locus-3</i> 4	Enrolme	nt No:	UP	ED
End Semester Examination, December 2018         Semester: III         Program: B. Tech- Electrical Engineering Time: 03 hrs.       Semester: III         Max. Marks: 100         Instructions: Attempt all the sections.         SECTION A (20 Marks)         SECTION A (20 Marks)         SECTION A (20 Marks)         Q and No does a tunnel diode work and analyze tunneling effect in tunnel diode with the performance characteristics.       Q and VD.         Q 2       Determine the state of diode for the circuit shown in Fig. (1) and find ID and VD.         Assume simplified model for the diode.         INOP INCLUSION         Q and Minimize the following multiple output functions         Fig. (1)         Q and Minimize the following multiple output functions         Fig. (1)         Q and Minimize the following multiple output functions         Fig. (1)         Q and Minimize the following multiple output functions         Fig. (1)       4       CO3         Q 4       (i) (123) <sub>10</sub> $\rightarrow$ (?) <i>Locus-3</i> (ii) (11010) <sub>2</sub> $\rightarrow$ (?) <i>Locus-3</i> (ii) (11010) <sub>2</sub> $\rightarrow$ (?) <i>Locus-3</i> 4				
Course:Analog and Digital Electronics (ECEG-2024) Program: B. Tech- Electrical Engineering Time: 03 hrs.Semester:II Max. Marks: 100Instructions: Attempt all the sections.SECTION A (20 Marks)SECTION A (20 Marks)SECTION A (20 Marks)Q 1How does a tunnel diode work and analyze tunneling effect in tunnel diode with the performance characteristics.MarksCOQ 2Determine the state of diode for the circuit shown in Fig. (1) and findIp andVp.IpQ 3Minimize the following multiple output functions Fi=Sm (0, 2, 6, 10, 11, 12, 13) + d (3, 4, 5, 14, 15) Fr=Sm (1, 2, 6, 7, 8, 13, 14, 15) + d (3, 5, 15)4CO3Q 4(i) (i)(123) to -?? Laces-3 (ii)(10110) 2 - (?) Convode4CO3				
Program: B. Tech-Electrical Engineering         Max. Marks: 100         Instructions: Attempt all the sections.         SECTION A (20 Marks)         SECTION A (20 Marks)         S.No.       Answer all the questions.         SECTION A (20 Marks)         Q 1       How does a tunnel diode work and analyze tunneling effect in tunnel diode with the performance characteristics.       Q 0         Q 2       Determine the state of diode for the circuit shown in Fig. (1) and find ID and VD. Assume simplified model for the diode.       4       CO2         Q 3       Minimize the following multiple output functions Fig. (1)         Fig. (1)         Q 3       Minimize the following multiple output functions Fi=Dm (0, 2, 6, 10, 11, 12, 13)+d (3, 4, 5, 14, 15)       4       CO3         Q 4       (i) (123) <sub>10</sub> - (?) Excerv3 (110110) <sub>2</sub> - (?) Excerv3 (110110)       4       CO3		End Semester Examination, December 2018		
Max. Marks: 100         Instructions: Attempt all the sections.         SECTION A (20 Marks)         SECTION A (20 Marks)         SECTION A (20 Marks)         Q1       How does a tunnel diode work and analyze tunneling effect in tunnel diode with the performance characteristics.       Marks       CO         Q2       Determine the state of diode for the circuit shown in Fig. (1) and find $I_D$ and $V_D$ . Assume simplified model for the diode.       4 CO2         Q2       Determine the state of diode for the circuit shown in Fig. (1) and find $I_D$ and $V_D$ . Assume simplified model for the diode.       4       CO2         Q3       Minimize the following multiple output functions $F_i = \sum m (0, 2, 6, 10, 11, 12, 13) + d (3, 4, 5, 14, 15) + F_2 = \sum m (1, 2, 6, 7, 8, 13, 14, 15) + d (3, 5, 15)       4       CO3         Q4       (i) (123) 10 \rightarrow (2) Lacces-3 (110110) 2 \rightarrow (2) Carces-3 (110110) 2 \rightarrow (2) Carces (110110)$			II	
SECTION A (20 Marks)         S. No.       Answer all the questions.       Marks       CO         Q1       How does a tunnel diode work and analyze tunneling effect in tunnel diode with the performance characteristics.       4       CO1         Q2       Determine the state of diode for the circuit shown in Fig. (1) and find $I_D$ and $V_D$ . Assume simplified model for the diode.       4       CO2 $2V \bigoplus$ $Ik\Omega$ IkO       4       CO2         Q3       Minimize the following multiple output functions Fi= $\Sigma m$ (0, 2, 6, 10, 11, 12, 13)+ d (3, 4, 5, 14, 15) F= $\Sigma m$ (1, 2, 6, 7, 8, 13, 14, 15)+ d (3, 5, 15)       4       CO3         Q4       (i) (123) <sub>10</sub> $\rightarrow$ (?) Excess-3 (110110) <sub>2</sub> $\rightarrow$ (?) Gray code       4       CO3			s: 100	
SECTION A (20 Marks)         S. No.       Answer all the questions.       Marks       CO         Q1       How does a tunnel diode work and analyze tunneling effect in tunnel diode with the performance characteristics.       4       CO1         Q2       Determine the state of diode for the circuit shown in Fig. (1) and find $I_D$ and $V_D$ . Assume simplified model for the diode.       4       CO2 $2V \bigoplus$ $Ik\Omega$ IkO       4       CO2         Q3       Minimize the following multiple output functions Fi= $\Sigma m$ (0, 2, 6, 10, 11, 12, 13)+ d (3, 4, 5, 14, 15) F= $\Sigma m$ (1, 2, 6, 7, 8, 13, 14, 15)+ d (3, 5, 15)       4       CO3         Q4       (i) (123) <sub>10</sub> $\rightarrow$ (?) Excess-3 (110110) <sub>2</sub> $\rightarrow$ (?) Gray code       4       CO3				
S. No.       Answer all the questions.       Marks       CO         Q1       How does a tunnel diode work and analyze tunneling effect in tunnel diode with the performance characteristics.       4       CO1         Q2       Determine the state of diode for the circuit shown in Fig. (1) and find $I_D$ and $V_D$ . Assume simplified model for the diode.       4       CO2         Q4 $I_{KO}$ $I_{KO}$ 4       CO2         Q3       Minimize the following multiple output functions $F_1=\summ (0, 2, 6, 10, 11, 12, 13) + d (3, 4, 5, 14, 15)$ 4       CO3         Q4 $(i) (123)_{10} \rightarrow (?)_{Excess^{-3}}$ $(i) (10110)_2 \rightarrow (?)_{Gray code}$ 4       CO3	Instruct	ions: Attempt all the sections.		
S. No.       Answer all the questions.       Marks       CO         Q1       How does a tunnel diode work and analyze tunneling effect in tunnel diode with the performance characteristics.       4       CO1         Q2       Determine the state of diode for the circuit shown in Fig. (1) and find $I_D$ and $V_D$ . Assume simplified model for the diode.       4       CO2         Q4 $I_{KO}$ $I_{KO}$ 4       CO2         Q3       Minimize the following multiple output functions $F_1=\summ (0, 2, 6, 10, 11, 12, 13) + d (3, 4, 5, 14, 15)$ 4       CO3         Q4 $(i) (123)_{10} \rightarrow (?)_{Excess^{-3}}$ $(i) (10110)_2 \rightarrow (?)_{Gray code}$ 4       CO3		SECTION A (20 Marks)		
Q 1How does a tunnel diode work and analyze tunneling effect in tunnel diode with the performance characteristics.CO1Q 2Determine the state of diode for the circuit shown in Fig. (1) and find $I_D$ and $V_D$ . Assume simplified model for the diode.4Q 4 $I_{\rm LO}$ $I_{\rm LO}$ Q 3Minimize the following multiple output functions $F_1=\sum m (1, 2, 6, 7, 8, 13, 14, 15) + d (3, 4, 5, 14, 15)$ $F_2=\sum m (1, 2, 6, 7, 8, 13, 14, 15) + d (3, 5, 15)$ 4Q 4 $(i) (123)_{10} \rightarrow (?)_{Excest-3}$ $(ii) (110110)_2 \rightarrow (?)_{Gray code}$ 4		SECTION A (20 Marks)		
performance characteristics.4COIQ2Determine the state of diode for the circuit shown in Fig. (1) and find $I_D$ and $V_D$ . Assume simplified model for the diode.4CO2Q4 $I_{KO}$ $I_{KO}$ 4CO3Q3Minimize the following multiple output functions $F_1=\sum m (0, 2, 6, 10, 11, 12, 13) + d (3, 4, 5, 14, 15)$ $F_2=\sum m (1, 2, 6, 7, 8, 13, 14, 15) + d (3, 5, 15)$ 4CO3Q4 $(i) (123)_{10} \rightarrow (?)_{Excess-3}$ $(ii) (110110)_2 \rightarrow (?)_{Gray code}$ 4CO3			Marks	CO
$\begin{array}{c c c c c c c c c c c c c c c c c c c $	Q 1		4	CO1
Assume simplified model for the diode.       4       CO2 $2V \bigoplus Ik\Omega \bigoplus Iik\Omega \bigoplus IikO IiiikO \bigoplus IikO IiikO IiikO IikO IikO IiikO IikO I$	Q 2	Determine the state of diode for the circuit shown in Fig. (1) and find $I_D$ and $V_D$		
$\begin{array}{c c c c c c c c c c c c c c c c c c c $		Assume simplified model for the diode.		
$\begin{array}{c c c c c c c c c c c c c c c c c c c $				
$\begin{array}{c c c c c c c c c c c c c c c c c c c $				
$\begin{array}{c c c c c c c c c c c c c c c c c c c $			4	CO2
Q 3       Minimize the following multiple output functions $F_1 = \sum m (0, 2, 6, 10, 11, 12, 13) + d (3, 4, 5, 14, 15)$ $F_2 = \sum m (1, 2, 6, 7, 8, 13, 14, 15) + d (3, 5, 15)$ 4       CO3         Q 4       (i) $(123)_{10} \rightarrow (?)_{Excess-3}$ (ii) $(110110)_2 \rightarrow (?)_{Gray code}$ 4       CO3		$2V (+)$ $1k\Omega \ge V$		
Q 3       Minimize the following multiple output functions $F_1 = \sum m (0, 2, 6, 10, 11, 12, 13) + d (3, 4, 5, 14, 15)$ $F_2 = \sum m (1, 2, 6, 7, 8, 13, 14, 15) + d (3, 5, 15)$ 4       CO3         Q 4       (i) $(123)_{10} \rightarrow (?)_{Excess-3}$ (ii) $(110110)_2 \rightarrow (?)_{Gray code}$ 4       CO3				
Q 3       Minimize the following multiple output functions $F_1 = \sum m (0, 2, 6, 10, 11, 12, 13) + d (3, 4, 5, 14, 15)$ $F_2 = \sum m (1, 2, 6, 7, 8, 13, 14, 15) + d (3, 5, 15)$ 4       CO3         Q 4       (i) $(123)_{10} \rightarrow (?)_{Excess-3}$ (ii) $(110110)_2 \rightarrow (?)_{Gray code}$ 4       CO3				
$\begin{array}{c c} F_{1} = \sum m (0, 2, 6, 10, 11, 12, 13) + d (3, 4, 5, 14, 15) \\ F_{2} = \sum m (1, 2, 6, 7, 8, 13, 14, 15) + d (3, 5, 15) \end{array} \qquad \begin{array}{c c} 4 & \mathbf{CO3} \\ \hline & & \\ (1) & (123)_{10} \rightarrow (?)_{Excess-3} \\ & & \\ (110110)_{2} \rightarrow (?)_{Gray \ code} \end{array} \qquad \begin{array}{c c} 4 & \mathbf{CO3} \\ \hline & & \\ 4 & \mathbf{CO3} \end{array}$				
$\begin{array}{c c} F_2 = \sum m (1, 2, 6, 7, 8, 13, 14, 15) + d (3, 5, 15) \\ \hline Q \ 4 \\ (i) \ (123)_{10} \rightarrow (?)_{Excess-3} \\ (ii) \ (110110)_2 \rightarrow (?)_{Gray \ code} \end{array} $	Q 3	• • •	1	CO3
Q 4 (i) $(123)_{10} \rightarrow (?)_{Excess-3}$ (ii) $(110110)_2 \rightarrow (?)_{Gray \ code}$ 4 CO3			-+	COJ
	Q 4	(122) $(2)$		
		$(110110)_2 \rightarrow (?)_{\text{Gray code}}$		~~~
$\binom{(11011)}{\text{Gray code}} \rightarrow (?)_2$		$(11011) \rightarrow (2)$	4	CO3
$\binom{(n)}{(iv)} (1275)_{BCD} \rightarrow (?)_2$		(1275) $(2)$		
Q 5 How combinational circuits are differing from the sequential circuits? Defend it with the specific reasons. 4 CO4	Q 5		4	<b>CO4</b>

	SECTION B (40 Marks)		
	Answer all the questions.		
Q 6	For what minimum value of propagation delay in each flip-flop (FF) will a 10-bit ripple counter skip a count when it is clocked at 10MHz?	10	CO4
Q 7	Attempt the both parts:		
	(a) Derive the expression for the efficiency of a Half-wave rectifier.	10	
	(b) The four diode used in a bridge rectifier circuit have forward resistance which may be considered constant at $1\Omega$ and infinite reverse resistance. The alternating supply voltage is 240 r.m.s. and load resistance is 480 $\Omega$ . Calculate: (i) mean load current (ii) power dissipated in each diode.	10	CO2
Q 8	Obtain the minimal expression for $F=\sum m$ (1, 2, 3, 5, 6, 7, 8, 9, 12, 15) using the Quine-McCluskey (Tabulation) method.	10	CO3
Q 9	Design a 2- input 2-output synchronous sequential circuit which produces an output $z=1$ , whenever any of the following input sequences 1100, 1010, or 1001 occurs. The circuit resets to its initial state after a 1 output has been generated.		
	OR	10	CO4
	Design and 4-bit binary data B <sub>4</sub> , B <sub>3</sub> , B <sub>2</sub> , B <sub>1</sub> to Gray code converter.		
	SECTION-C (40 Marks)		1
	Answer all the questions.		
Q 10	A lawn-sprinkling system is controlled automatically by certain combinations of the following variables,		
	<ul><li>(i) Season (S=1, if summer; 0, otherwise</li><li>(ii) Moisture content of soil (M=1, if High; 0, if Low)</li></ul>		
	(iii) Outside temperature (T= 1, if High; 0, if Low)		
	(iv) Outside humidity (H= 1, if High; 0, if Low)		
	The sprinkler is turned on under any of the following circumstances,	20	
	The sprinkler is turned on under any of the following circumstances, (1) The moisture content is Low in winter	20	CO3
	<ul><li>(1) The moisture content is Low in winter</li><li>(2) The temperature is High and the moisture content is Low in summer</li></ul>	20	CO3
	<ul><li>(1) The moisture content is Low in winter</li><li>(2) The temperature is High and the moisture content is Low in summer</li><li>(3) The temperature is High and the humidity is High in summer</li></ul>	20	CO3
	<ul><li>(1) The moisture content is Low in winter</li><li>(2) The temperature is High and the moisture content is Low in summer</li></ul>	20	CO3

Q 11	• Attempt both the parts:		
	<ul><li>(a) Design a pulse generator using indirect logic to produce the following waveforms shown in Fig. (2) as,</li></ul>		
	0 1 0 0 1 1 0 0 0		
	0 1 1 1 1 1 1 0 0	10+10	CO4/ CO3
	Fig. (2)		
	(b) Analyze the operation of encoder and design an Octal –to-Binary (8-line to 3-line) encoder.		
	OR		
	Design a combinational circuit that accepts a 3-bit BCD number (A, B, D) and generates an output binary number equal to the square of the input numbers.		