## UNIVERSITY OF PETROLEUM AND ENERGY STUDIES End Semester Examination, April/May 2018

Course: Embedded Systems Semester: IV

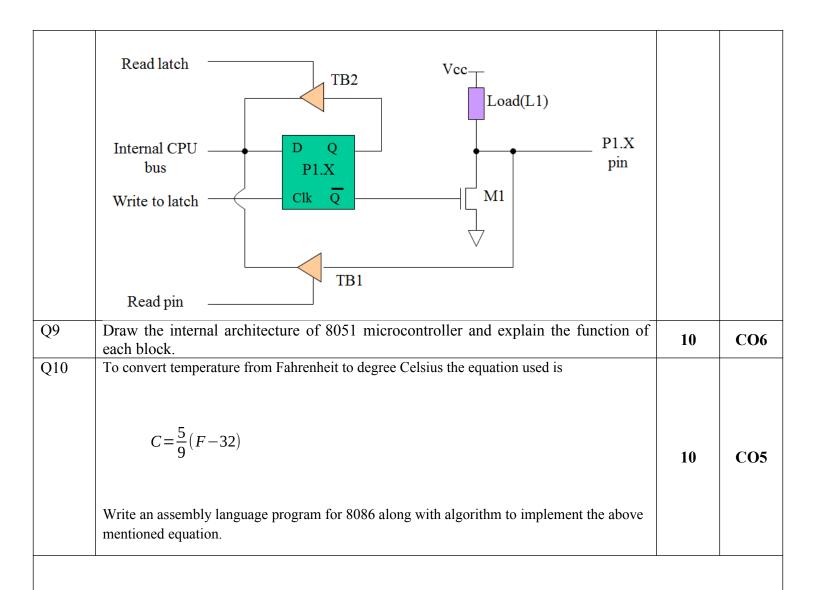
Course: Embedded Systems Program: B. Tech Mechatronics

Time: 03 hrs. Max. Marks: 100

Instructions: For every assembly and C code mention the comments of each instruction.

## **SECTION A**

S. No.	American all the assertions	26.1	
	Answer all the questions	Marks	CO
Q1	Various microcontrollers are available in the market, what are the things which one	4	CO4
	should keep in mind before purchasing the microprocessor for an application.		
Q2	Explain the function of the following program:		
	Program: MVI A, BYTE1		
	ORA A		
	JM OUTPRT		CO1
	OUT 01H		
	HLT	4	
	OUTPRT: CMA		
	ADI 01H		
	OUT 01H		
	HLT		
	In the above program if BYTE1 = A7H, what will be displayed at port 01H?		
Q3	Load the bit pattern 91H in register B and 87H in register C of 8085 microprocessor.		CO2
	Mask all the bits except $D_0$ from register B and C. If $D_0$ is at logic 1 in both registers,	4	
	turn on the light connected to the D <sub>0</sub> position of output port 01H; otherwise, turn of	-	
	the light.		
Q4	Elucidate the advantage of pipeline architecture utilized in 8086. Highlight the	4	CO <sub>3</sub>
	differences between 8085 & 8086 microprocessors.	-	
Q5	Write 8085 assembly language program to subtract two 8-bit numbers ABH and 79H	4	CO2
	using only two instructions. Mention the result and status of flags.	•	
	SECTION B		
S. No.	Answer any four questions	Marks	CO
Q6	If the task is to design an Engine Control Unit (ECU) of a very high end vehicle which		
٧º	works on RTOS, as a mechanical engineer which device will you choose between a)		COF
	microprocessor and b) microcontroller and why? Justify your response with suitable	10	CO <sub>5</sub>
	<b>generalized</b> block diagrams of both explaining the function of each block.		
Q7	In 8051 write a C code to connect 7-segment display system which should two 7-	10	CO4
	segment to display the count 100 ticks from 00 to 99 in an incremental manner.		
Q8	The hardware structure of an I/O pin of 8051 is given in the figure below. Explain	10	CO <sub>6</sub>
	the complete architecture and explain the step by step process of		
	i) Writing 1 and 0 to pin P1.X		
	ii) Reading High and Low at input pin P1.X		



## **SECTION-C**

S. No.	Answer any two questions	Marks	CO
Q11	Design a decoder and memory interface system for 8085 microprocessor to connect 32kB of EPROM using 8kB of EPROM and 32kB of RAM using 8kB of RAM.	20	CO3
Q12	Consider two 64-bit numbers given below	20	CO5

	i) 1234567890ABCDEF H ii) 985401AD77654321 H		
	Design an adder system for 8086 processor by writing the code in assembly language for adding both the numbers. Draw the flow chart of the adder and write its algorithm. Comment on the result obtained.		
Q13	Design a networked embedded notice board system system using 8051that can display the message "MECHATRONICS" in the first line and "UPES" in the second line of LCD with the following assumptions		
	<ul> <li>i) Connect Port 0 of 8051 to data pins of LCD</li> <li>ii) Connect Port 2 of 8051 to control pins of LCD</li> <li>Write the C program along with algorithm.</li> </ul>	20	CO6

Name:

**Enrolment No:** 

