Name:

Enrolment No:



UPES End Semester Examination, May 2024

Course:	Programmable Logic Controller & HMI	Semester:	VI
Program:	B. Tech (Mechatronics Engineering)	Time:	03 hrs.
Course Code:	ECEG 3055	Max. Marks:	100

Instructions: This question paper has three sections, Section A, Section B, and Section C.

SECTION A (5Qx4M=20Marks)				
S. No.		Marks	СО	
Q 1	Define onboard and inline I/O's. Explain different onboard and inline I/O's used in PLC.	4	CO1	
Q 2	Compare the different memory storage characteristics of any variable in the memory element.	4	CO2	
Q 3	Outline the sequence of events involved in a single PLC scan cycle.	4	CO3	
Q 4	Describe the similarities and differences between PLC ladder logic and relay ladder logic.	4	CO3	
Q 5	Explain the set/reset coil with a neat diagram and explain them briefly.	4	CO 1	
	SECTION B (4Qx10M= 40 Marks)			
Q 6	Illustrate the process of setting up analog inputs and output ports for all the configurations in L20 PLC. OR Write a ladder logic program to make the bulb ON/OFF continuously with one input switch. Assume ON and OFF time is 3 milliseconds.	10	CO3	
Q 7	 Write a ladder logic program for the following conditions: 1. When start input S0 is given then the following should happen Fan1 and Fan2 should come ON Mains contactor(K1) should Close System on Lamp(L1) should come ON 2. When Fan1 and Fan2 fails, the standby fan Fan3 should come ON 3. When two fans out of provided three fans fail then the following should happen Mains contactor (K1) should drop System ON lamp(L1) should flash at 5 Hz frequency 	10	CO4	



