Name:

Enrolment No:



UPES

End Semester Examination, May 2024

Program Name: B.Tech Automotive Design Engineering Course Name: Microprocessor Based Control System

Course Name: Microprocessor Dased Control

Course Code: ECEG-2056

Nos. of page(s): 2

Instructions: Assume any data in the design, if required. SECTION-A (5Q x 4M = 20 Marks)

S. No.				Marks	CO
Q.1	What is the different between microprocessor and microcontroller-based system.			4	CO1
Q.2	Detail the PSW status format of 8085 microprocessor and different flag status with examples.			4	CO2
Q.3		the logic diagram of address decoder multiplexer with complete detail of the ble and Boolean equations.			CO1
Q.4	List the various types of	instructions in 8085 microprocessors with examples.			CO2
Q.5	Interface the 8051 microcontrollers with 8 LEDs and write the program for blinking the LEDs in alternate fashion.			4	CO2
	1	SECTION B (4Q x	10 M = 40 Marks	1	
	ot all the questions.				
Q.6		d to addressing modes in microprocessor-based systems. addressing modes of 8085 microprocessor with examples.		10	CO1
Q.7	1	priority of interrupts of 8085 microprocessor. Explain the e when multiple interrupts are assigned to microprocessor. Draw ram to support your answer.			CO3
Q.8	Write an assembly program for 8085 to find the largest number in an array. Table 1 Data array				
	Memory Address	Data	7	10	CO2
	2500 H	06 H			
	2501 H	38 H			
	2502 H	94 H			
	2503 H	EB H			
	2504 H	A8 H	-		
Q.9	systems. Take an example the control strategy req	le of ECU interface w uired for anti-lock b	controller for automotive embedded with the control unit of a car and show praking systems (ABS) and traction ntrol during braking and acceleration.	10	CO4

Semester: IV Time: 3 hrs Max. Marks: 100

	Explain the multiplexing and demultiplexing is microprocessor-based control system.		
	SECTION-C (2Q x 2M = 40 Marks)		
	pt any <i>two</i> of the followings		
Q.10	 (a) Detail the functional block diagram of 8085 microprocessor with the complete functionality of individual block (b) Calculate the time required to execute the instruction STA 9000 H, if the XTAL frequency is 3 MHz, also explain its timing diagram with the following data shown in Table 2. Table 2 STA instruction 	10+10	CO2
	800F STA 9000 H 32		
	8010 00		
	8011 90		
Q.11	 (a) Detail the pin layout of 8085 microprocessor with the complete functionality of individual pin. (b) Draw the block diagram of different types of control systems, Discuss the different functional blocks of power window control. 	10+10	CO3
	other cars found in the garage. Each car that comes out will automatically be taken off. When 100 cars park, a signal will turn on signaling that a garage is full and notifying other drivers not to enter because there is no space available. for four enter because there is no space available. for 100 cars Fig. 1 Signal from a sensor at the garage entrance sets bit IR200.00. This bit is a condition for execution of the following two instructions in a program. The first instruction resets carry bit CY (it is always done before some other calculation that would influence it), and the other instruction adds one to several cars in word HR00, and a	20	CO4